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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/512,149
Filing Date February 23, 2000
Inventor Vishnu K. Agarwal
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner M. Pizarro Crespo
Attorney's Docket No. MI22-1322
Title: Integrated Circuitry Including a Capacitor with an Amorphous and a Crystalline High
K Capacitor Dielectric Region



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents

Washington, D.C. 20231

REMARKS

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these patents is respectfully requested.

Respectfully submitted,

Date: Dec 5, 2001

By: Bernard Berman

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